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<u>L21</u>	L18 and l6	27	<u>L21</u>
<u>L20</u>	L18 and l5	10	<u>L20</u>
<u>L19</u>	L18 and l4	81	<u>L19</u>
<u>L18</u>	L2 and (select\$5 or mux or multiplex\$4) near15 writ\$5 near25 read\$5	159	<u>L18</u>
<u>L17</u>	L14 and l6	11	<u>L17</u>
<u>L16</u>	L14 and l5	0	<u>L16</u>
<u>L15</u>	L14 and l4	12	<u>L15</u>
<u>L14</u>	l3 and writ\$5 near25 read\$5	33	<u>L14</u>
<u>L13</u>	l6 and L10	52	<u>L13</u>
<u>L12</u>	l5 and L10	11	<u>L12</u>
<u>L11</u>	l4 and L10	121	<u>L11</u>

<u>L10</u>	L2 and (select\$5 or mux or multiplex\$4) near15 branch\$3	176	<u>L10</u>
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<u>L9</u>	l3 and l6	13	<u>L9</u>
<u>L8</u>	l3 and l5	0	<u>L8</u>
<u>L7</u>	l3 and l4	18	<u>L7</u>
<u>L6</u>	(711/118-221)[CCLS]	19631	<u>L6</u>
<u>L5</u>	(712/30-42)[CCLS]	1757	<u>L5</u>
<u>L4</u>	(712/2-300)[CCLS]	10993	<u>L4</u>
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<u>L3</u>	L2 and (select\$5 or mux or multiplex\$4) near15 (prom\$1 or eeprom\$1 or eprom\$1 or rom\$1 or (opcode\$1 or program) near3 (memor\$4 or stor\$4))	41	<u>L3</u>
<u>L2</u>	L1 and (fetch\$6 or prefetch\$5) near5 address\$4 near12 (cache or register near1 file)	371	<u>L2</u>
<u>L1</u>	base near5 address\$4 near15 (stor\$5 or sav\$3 or kept or keep\$3 or hold\$3 or held)	8287	<u>L1</u>

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IEEE CNF IEEE Conference Proceeding

IEEE CNF IEEE Conference Proceeding

IEEE STD IEEE Standard

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